

Electronic Acknowledgement Receipt

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| EFS ID: | 8367745 |
| Application Number: | 10596944 |
| International Application Number: | |
| Confirmation Number: | 2642 |
| Title of Invention: | Circuit layout compaction using reshaping |
| First Named Inventor/Applicant Name: | Robert Lee Maziasz |
| Customer Number: | 34814 |
| Filer: | Adam D. Sheehan |
| Filer Authorized By: | |
| Attorney Docket Number: | SC12656TS |
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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes)/ Message Digest | Multi Part /.zip | Pages (if appl.) |
|-----------------|----------------------|----------------------------|---|------------------|------------------|
| 1 | | SC12656TS_FOA_Response.pdf | 87165 f89ed3ecea493cedb107fe86466512f73dbf171f | yes | 7 |

| | Multipart Description/PDF files in .zip description | | |
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| | Document Description | Start | End |
| | Amendment After Final | 1 | 1 |
| | Claims | 2 | 5 |
| | Applicant Arguments/Remarks Made in an Amendment | 6 | 7 |
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